
||Jai Sri Gurudev||
B.G.S INSTITUTE OF TECHNOLOGY
 B.G.Nagara-571448

Department of Electronics & Communication Engineering.

TIME TABLE
2017-18 EVEN

Semester: IV
Section: A

Class Room: TB-106

Days	9:00-9:55AM	9:55-10:50AM	T E A C H E R S	11:00-11:55AM	11:55-12:50PM	L U N C H B R E A K	1:45-2:40 PM	2:40-3:35 PM	3:35-4:30 PM		
Monday	15 EC45	15 EC46			15 MAT41		15 EC42		HR TRAINING		
Tuesday	15 EC44	15 EC45			15 MAT41		15 EC43		MP / LIC LAB		
Wednesday	15 EC44	15 EC42			15 EC45		15 EC46		MP / LIC LAB		
Thursday	15 EC43	15 EC45			15 EC44		15 EC46		15 MAT 41	15 EC42	15 EC43
Friday	15 EC42	15 EC43			15 EC44		15 EC46		15 EC46	15 EC45	15 MAT 41
Saturday	15 EC43	15 EC42			15 MAT41		15 EC44				

Subject Code

15 MAT 41
15 EC 42
15 EC 43
15 EC 44
15 EC 45
15 EC 46
15 ECL 47
15 ECL 48



Subject Name

Engg. Mathematics-IV
Microprocessor
Control System
Signals and System
Principles of Communication system
Linear Integrated Circuits
Microprocessor Lab
LIC Lab

Staff Name

Parashivamurthy H L
Dr. Naveen B
Puneeth Kumar G B
Anusha M N
Priyanka R
Kavitha B C
Dr. Naveen B /Prashantha N C
Dr. M B Anandaraju/ Kavitha B C/Anusha M N



Prepared by:	Dr. M B Anandaraju	Approved by:	Dr. B K Narendra
Signature		Signature	

Days	9:00-9:55AM	9:55-10:50AM	T E A B R E A K	11:00-11:55AM	11:55-12:50PM	L U N C H B R E A K	1:45-2:40 PM	2:40-3:35 PM	3:35-4:30 PM
Monday	15 MAT41	15 EC42		15 EC43	15 EC45		15 EC46	15 EC44	15 MAT 41
Tuesday	15 EC43	15 EC45		15 MAT41	15 EC46		HR TRAINING		
Wednesday	15 EC45	15 EC42		15 EC44	15 EC43		15 EC44	15 EC46	15 EC43
Thursday	15 EC42	MP Lab		/ LIC Lab			15 MAT41	15 EC42	15 EC45
Friday	15 EC44	15 EC46		15 EC42	15 EC43		MP / LIC LAB		
Saturday	15 MAT41	15 EC 46		15 EC45	15 EC44				

Subject Code

15 MAT 41
15 EC 42
15 EC 43
15 EC 44
15 EC 45
15 EC 46
15 ECL 47
15 ECL 48

Subject Name

Engg. Mathematics-IV
Microprocessor
Control System
Signals and System
Principles of Communication system
Linear Integrated Circuits
Microprocessor Lab
LIC Lab

Staff Name

Chaithra
Shalini K J
Puneeth Kumar G B
Kokila K S
Prafulla P S
Nandini S
Dr. Naveen B/Kokila K S
Puneeth Kumar G B/Nandini S



Prepared by:	Dr. M B Anandaraju	Approved by:	Dr. B K Narendra
Signature		Signature	

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 Department of Electronics & Communication Engineering.

TIME TABLE

Semester: IV
 Section: C

2017-18 EVEN

Class Room: CR-204

Days	9:00-9:55AM	9:55-10:50AM		11:00-11:55AM	11:55-12:50PM		1:45-2:40 PM	2:40-3:35 PM	3:35-4:30 PM
Monday	15 EC43	15 EC44	T E A	15 EC45	15 EC46	L U N C H	MP / LIC LAB		
Tuesday	15 EC42	15 MAT41		HR TRAINING			15 EC46	15 EC43	15 EC45
Wednesday	15 EC45	15 EC46	B R E A K	15 MAT41	15 EC42	B R E A K	15 MAT41	15 EC42	15 EC44
Thursday	15 EC44	15 EC46		15 EC43	15 EC42		MP / LIC LAB		
Friday	15 EC42	15 EC46		15 EC45	15 EC43		15 MAT41	15 EC44	15 EC43
Saturday	15 EC43	15 EC44		15 EC45	15 MAT41				

Subject Code

15 MAT 41
 15 EC 42
 15 EC 43
 15 EC 44
 15 EC 45
 15 EC 46
 15 ECL 47
 15 ECL 48

Subject Name

Engg. Mathematics-IV
 Microprocessor
 Control System
 Signals and System
 Principles of Communication system
 Linear Integrated Circuits
 Microprocessor Lab
 LIC Lab

Staff Name

Yuvaraju B K
 Shalini K J
 Mohan Kumar k S
 Anusha M N
 Priyanka R
 Kavitha B C
 Jayanth Dwijesh H P/Shalini K J
 Mohan Kumar k S/Sandeep S V



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Signature		Signature	

Days	9:00-9:55AM	9:55-10:50AM	T E A B R E A K	11:00-11:55AM	11:55-12:50PM	L U N C H B R E A K	1:45-2:40 PM	2:40-3:35 PM	3:35-4:30 PM
Monday	15EC64	15EC61		15EC655	15EC62		Embed/CCN Lab		
Tuesday	HR Training			15EC63	15EC64		15EC62	15EC661	15EC65
Wednesday	15EC661	15EC61		15EC63	15EC655		Embed/CCN Lab		
Thursday	15EC63	15EC661		15EC61	15EC62		Embed/CCN Lab		
Friday	15EC62	15EC655		15EC661	15EC655		15EC61	15EC64	15EC63
Saturday	15EC61	15EC64		15EC62	15EC63				

Subject Code

Subject Name

Staff Name

15 EC 61
15 EC 62
15 EC 63
15 EC 64
15 EC 655
15 EC 661
15 ECL 67
15 ECL 68

Digital Communication
ARM Microcontrollers & Embedded Systems
VLSI Design
Computer Communication Networks
Microelectronics
Data Structures Using C++
Embedded Controller Lab
Computer Networks Lab

Prabhavathi K
Balaji B S
Srividya C N
Nischitha S
Nandeesh M
Manoj Kumar S B
Manoj Kumar S B/ Prafulla P S
Prabhavathi K/Nischitha S



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Days	9:00-9:55AM	9:55-10:50AM	11:00-11:55AM	11:55-12:50PM	L U N C H	1:45-2:40 PM	2:40-3:35 PM	3:35-4:30 PM
Monday	HR Training		15EC661	15EC61	B R E A K	15EC64	15EC63	15EC655
Tuesday	15EC63	15EC655	15EC62	15EC661		Embed/CCN Lab		
Wednesday	15EC655	15EC64	15EC661	15EC61		15EC655	15EC62	15EC63
Thursday	15EC655	Embed/	CCN Lab			15EC661	15EC61	15EC62
Friday	15EC64	15EC61	15EC62	15EC63		Embed/CCN Lab		
Saturday	15EC62	15EC63	15EC61	15EC64				

Subject Code

Subject Name

Staff Name

15 EC 61
15 EC 62
15 EC 63
15 EC 64
15 EC 655
15 EC 661
15 ECL 67
15 ECL 68

Digital Communication
ARM Microcontrollers & Embedded Systems
VLSI Design
Computer Communication Networks
Microelectronics
Data Structures Using C++
Embedded Controller Lab
Computer Networks Lab

Prabhavathi K
Balaji B S
Srividya C N
Nischitha S
Nandeesh M
Manoj Kumar S B
Nandeesh M/Srividya C N
Raghavendra L R/Priyanka R



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Days	9:00-9:55AM	9:55-10:50AM	T E A B R E A K	11:00-11:55AM	11:55-12:50PM	L U N C H B R E A	1:45-2:40 PM	2:40-3:35 PM	3:35-4:30 PM
Monday	10 EC 835	10 EC81		10 EC843	10 EC82		Project/Seminar work		
Tuesday	10 EC82	10 EC835		10 EC 843	10 EC81				
Wednesday	10 EC843	10 EC82		10 EC835	10 EC81				
Thursday	10 EC82	10 EC81		10 EC835	10 EC843				
Friday	Project/Seminar work								
Saturday	Project/Seminar work								